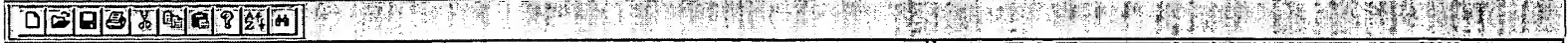


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	841	455/194.2 or 455/127.2 or 455/232.1	US-PGPUB; USPAT; EPO	OR	ON	2005/10/31 10:22
L2	403	1 and (gain near3 control\$4).clm.	US-PGPUB; USPAT; EPO	OR	ON	2005/10/31 10:23
L3	3	2 and (power near5 ratio near5 monitor\$3).clm.	US-PGPUB; USPAT; EPO	OR	ON	2005/10/31 10:23
L4	1	3 and (squar\$3 near3 circuit).clm.	US-PGPUB; USPAT; EPO	OR	ON	2005/10/31 10:24



- Drafts
 - BRS:
 - Pending
 - Active
 - L1: (841) 455/194.2 or 455/127.2 or 455/232.1
 - L2: (403) 1 and (gain near3 control\$4).clm.
 - L3: (3) 2 and (power near5 ratio near5 monitor\$3).clm.
 - L4: (1) 3 and (squar\$3 near3 circuit).clm.
 - Failed
 - Saved
 - S1: (1) ("6115406").PN.
 - S2: (67447) "455"/\$.ccls.
 - S3: (6168) S2 and (gain near3 control\$4)
 - S4: (1816) S3 and ("power amplifier" or PA)
 - S5: (9) S4 and (power near5 ratio near5 monitor\$3)
 - S6: (6) S5 and (variable near5 gain)
 - S7: (67447) "455"/\$.ccls.
 - S8: (6168) S7 and (gain near3 control\$4)
 - S9: (1816) S8 and ("power amplifier" or PA)
 - S10: (9) S9 and (power near5 ratio near5 monitor\$3)
 - S11: (6) S10 and (variable near5 gain)
 - S12: (11750) (variable near3 gain) and (gain near3 control\$4)
 - S13: (2435) S12 and ("power amplifier" or PA)
 - S14: (6) S13 and "second order distortion"

United States
(12) Patent Application Publication (10) Pub. No.: US 2002/0123315 A1
Hayashihara (19) Pub. Date: Sep. 5, 2002

(34) RADIO COMMUNICATION TERMINAL AND GAIN CONTROL CIRCUIT FOR THE SAME (37) U.S. CL. CLASS. 455/194.2; 455/241.1

(70) Inventor: Mikio Hayashihara, Hayashihara (35) ABSTRACT

Correspondence Address:
ONELOS SPRING MEZZELAND MAHER &
NEUSTADT PC
FOURTH FLOOR
175 ALPHEON DRIVE HIGHWAY
ARLINGTON, VA 22202 (US)

(21) Appl. No.: 10085735
(22) Filed: Mar. 1, 2002
(43) Foreign Application Priority Data
Mar. 1, 2001 (JP) 2001-029314

Publication Classification
(51) Int. Cl. 7 H04B 1/16

A gain control circuit has a transmission power amplifier that amplifies a transmission signal to a predetermined level. An adjuster circuit has a power ratio monitor that outputs a value of a transmission channel corresponding to an adjusted channel loss power to a gain control circuit. The gain control circuit has a gain control circuit that outputs a gain control signal to the transmission power amplifier, and outputs the gain control signal to the transmission power amplifier. A power supply control circuit has a power supply control circuit that outputs a gain control signal to the transmission power amplifier with use of the ACPR monitor value outputted from the adjuster circuit. The gain control circuit has a gain control circuit that outputs a gain control signal to the transmission power amplifier. A transmission signal level variable section controls a gain of a transmission signal path on the basis of a transmission level monitor value outputted from the adjuster circuit. The gain control circuit has a gain control circuit that outputs a gain control signal to the transmission power amplifier.

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current	Ret	Inventor	S
1		<input checked="" type="checkbox"/>	US 20020123315 A1	20020905	9	Radio communication terminal and gain control circuit for the same	455/194.2	455/241.1		Hayashihara, Mikio	<input checked="" type="checkbox"/>

Hits Details HTML

Ready